

**PATENT APPLICATION
IN THE UNITED STATES PATENT AND TRADEMARK OFFICE**

Application Serial No: 10/061,695	Attorney Docket No: BP2175
Application Filing Date: 02-01-2002	Inventor(s): Sribalan Santhanam, et al.
Examiner: Wang, Albert C.	
Art Group: 2115	Patent No: 6,971,038
Confirmation No: 1574	Patent Issue Date: 11-29-2005
Title: Clock gating of sub-circuits within a processor execution unit responsive to instruction latency counter within processor issue circuit	

Date: 10-24-2008

ATTN: Certificate of Correction Branch
Commissioner for Patents
P.O. Box 1450
Alexandria, VA 22313-1450

REQUEST FOR CERTIFICATE OF CORRECTION

After reviewing the issued U.S. patent referenced above, the Applicant notices one or more error(s) that was/were apparently made through the fault of the United States Patent and Trademark Office (USPTO).

The Applicant respectfully requests that the Director issue a Certificate of Correction, without charge to the Applicant, to be recorded in the records of U.S. patents.

The Applicant respectfully believes that no fees are due in accordance with 35 U.S.C. § 254.

These corrections of the mistake(s) made by the USPTO are also depicted on the enclosed Certificate of Correction - Form PTO/SB/44 (09-07) (1 pg).

Errors made through fault of the USPTO.

Column 16, line 11, in Claim 14: replace "circuits" with --circuit--.

Column 16, line 13, in Claim 14: replace "second circuit" with --a second circuit--.

The Office is invited to contact the undersigned by telephone or facsimile with any questions or concerns whatsoever.

RESPECTFULLY SUBMITTED,
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UNITED STATES PATENT AND TRADEMARK OFFICE CERTIFICATE OF CORRECTION

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PATENT NO. : 6,971,038

APPLICATION NO.: 10/061,695

ISSUE DATE : 11-29-2005

INVENTOR(S) : Sribalan Santhanam, et al.

It is certified that an error appears or errors appear in the above-identified patent and that said Letters Patent is hereby corrected as shown below:

Column 16, line 11, in Claim 14: replace "circuits" with --circuit--.

Column 16, line 13, in Claim 14: replace "second circuit" with --a second circuit--.

MAILING ADDRESS OF SENDER (Please do not use customer number below):

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This collection of information is required by 37 CFR 1.322, 1.323, and 1.324. The information is required to obtain or retain a benefit by the public which is to file (and by the USPTO to process) an application. Confidentiality is governed by 35 U.S.C. 122 and 37 CFR 1.14. This collection is estimated to take 1.0 hour to complete, including gathering, preparing, and submitting the completed application form to the USPTO. Time will vary depending upon the individual case. Any comments on the amount of time you require to complete this form and/or suggestions for reducing this burden, should be sent to the Chief Information Officer, U.S. Patent and Trademark Office, U.S. Department of Commerce, P.O. Box 1450, Alexandria, VA 22313-1450. DO NOT SEND FEES OR COMPLETED FORMS TO THIS ADDRESS. SEND TO: **Attention Certificate of Corrections Branch, Commissioner for Patents, P.O. Box 1450, Alexandria, VA 22313-1450.**

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